

Notice of References Cited	Application/Control No. 09/996,446	Applicant(s)/Patent Under Reexamination DOWNEY ET AL.	
	Examiner Jermele M. Hollington	Art Unit 2829	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,037,235	03-2000	Narwankar et al.	438/396
	C	US-6,555,453 B1	04-2003	Xiang et al.	438/581
	D	US-6,767,808 B2	07-2004	Ryoo, Chang-Woo	438/510
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	N	JP 2001144095 A	05-2001	Japan	BOKU et al.	H01L 21/324
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NON-PATENT DOCUMENTS

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	U	Mullet et al, Device Electronics for Integrated Circuits, 1986, John Wiley & Sons, Second Edition, pages 79-82
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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